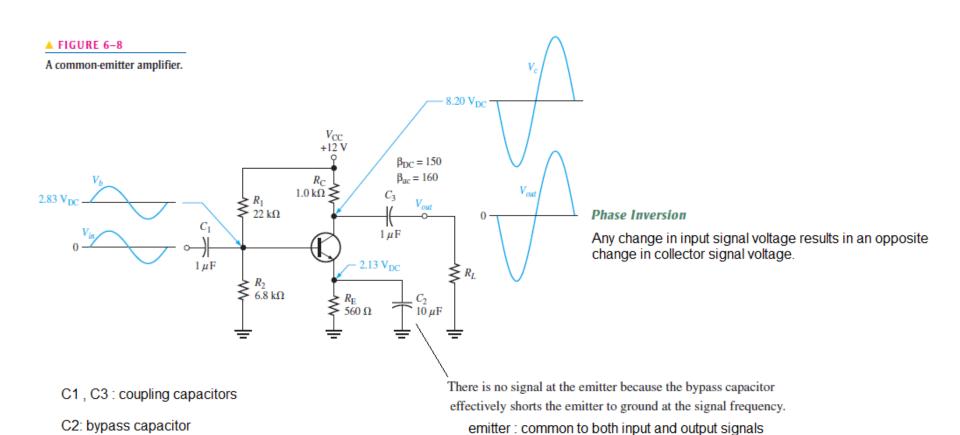
☐ CE amplifiers exhibit **high voltage gain** and **high current gain**.



DC Analysis

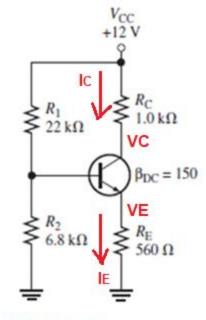
- All capacitors OPEN due to dc bias.
- Theveninizing the bias circuit and apply KVL to the base emitter circuit

$$\begin{split} R_{\rm TH} &= \frac{R_1 R_2}{R_1 + R_2} = \frac{(6.8 \, \text{k}\Omega)(22 \, \text{k}\Omega)}{6.8 \, \text{k}\Omega + 22 \, \text{k}\Omega} = 5.19 \, \text{k}\Omega \\ V_{\rm TH} &= \left(\frac{R_2}{R_1 + R_2}\right) V_{\rm CC} = \left(\frac{6.8 \, \text{k}\Omega}{6.8 \, \text{k}\Omega + 22 \, \text{k}\Omega}\right) 12 \, \text{V} = 2.83 \, \text{V} \end{split}$$

$$I_{\rm E} = \frac{V_{\rm TH} - V_{\rm BE}}{R_{\rm E} + R_{\rm TH}/\beta_{\rm DC}} = \frac{2.83 \text{ V} - 0.7 \text{ V}}{560 \Omega + 34.6 \Omega} = 3.58 \text{ mA}$$
 $I_{\rm C} \cong I_{\rm E} = 3.58 \text{ mA}$
 $V_{\rm E} = I_{\rm E}R_{\rm E} = (3.58 \text{ mA})(560 \Omega) = 2 \text{ V}$

$$V_{\rm B} = V_{\rm E} + 0.7 \,\text{V} = 2.7 \,\text{V}$$

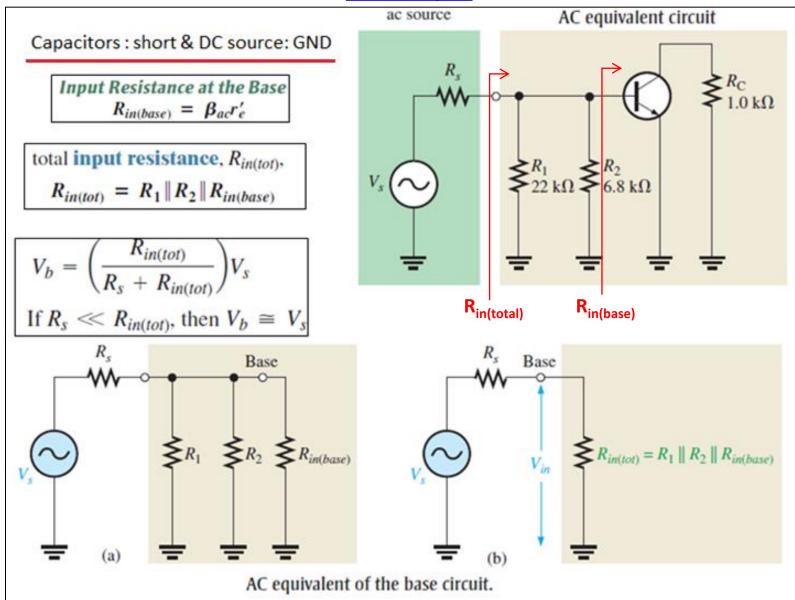
 $V_{\rm C} = V_{\rm CC} - I_{\rm C}R_{\rm C} = 12 \,\text{V} - (3.58 \,\text{mA})(1.0 \,\text{k}\Omega) = 8.42 \,\text{V}$
 $V_{\rm CE} = V_{\rm C} - V_{\rm E} = 8.42 \,\text{V} - 2 \,\text{V} = 6.42 \,\text{V}$



▲ FIGURE 6-9

DC equivalent circuit for the amplifier in Figure 6–8.

AC Analysis



Input Resistance and Output Resistance

Input Resistance at the Base

The base voltage is

and since $I_e \cong I_c$,

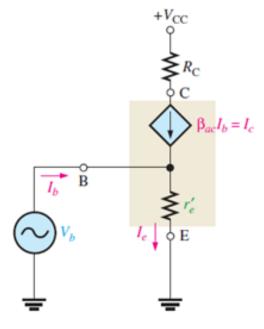
Substituting for V_b and I_b ,

$$R_{in(base)} = \frac{V_{in}}{I_{in}} = \frac{V_b}{I_b}$$
$$V_b = I_e r'_e$$

$$I_b \cong \frac{I_e}{\beta_{ac}}$$

$$R_{in(base)} = \frac{V_b}{I_b} = \frac{I_e r'_e}{I_e / \beta_{ac}}$$

$$R_{in(base)} = \beta_{ac}r'_e$$



▲ FIGURE 6-12

r-parameter transistor model (inside shaded block) connected to external circuit.

Output Resistance

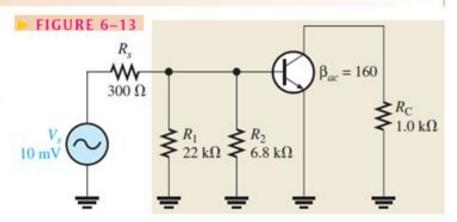
$$R_{out} \cong R_{\rm C}$$

EXAMPLE 6-3

Determine the signal voltage at the base of the transistor in Figure 6–13. This circuit is the ac equivalent of the amplifier in Figure 6–8 with a 10 mV rms, 300 Ω signal source. I_E was previously found to be 3.80 mA.

emitter resistance
$$r'_e \approx \frac{25 \text{ mV}}{I_E} = \frac{25 \text{ mV}}{3.80 \text{ mA}} = 6.58 \Omega$$

$$R_{in(base)} = \beta_{ac}r'_e = 160(6.58 \Omega) = 1.05 k\Omega$$



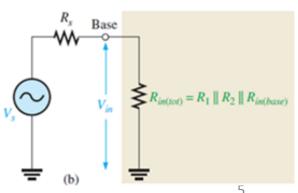
Next, determine the total input resistance viewed from the source.

$$R_{in(tot)} = R_1 \| R_2 \| R_{in(base)} = \frac{1}{\frac{1}{22 \text{ k}\Omega} + \frac{1}{6.8 \text{ k}\Omega} + \frac{1}{1.05 \text{ k}\Omega}} = 873 \Omega$$

Base $R_1 \implies R_2 \implies R_{in(base)}$

The source voltage is divided down by R_s and $R_{in(tot)}$, so the signal voltage at the base is the voltage across $R_{in(tot)}$.

$$V_b = \left(\frac{R_{in(tot)}}{R_s + R_{in(tot)}}\right) V_s = \left(\frac{873 \Omega}{1173 \Omega}\right) 10 \text{ mV} = 7.44 \text{ mV}$$



Voltage Gain (Av)

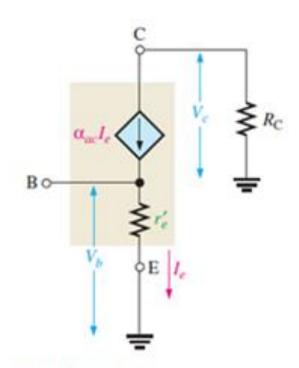
$$A_v = \frac{V_{out}}{V_{in}} = \frac{V_c}{V_b}$$

Notice in the figure that $V_c = \alpha_{ac}I_eR_C \equiv I_eR_C$

and
$$V_b = I_e r'_e$$
.

$$A_{v} = \frac{I_{e}R_{C}}{I_{e}r'_{e}}$$

$$A_v = \frac{R_C}{r'_e}$$



▲ FIGURE 6-14

Model circuit for obtaining ac voltage gain.

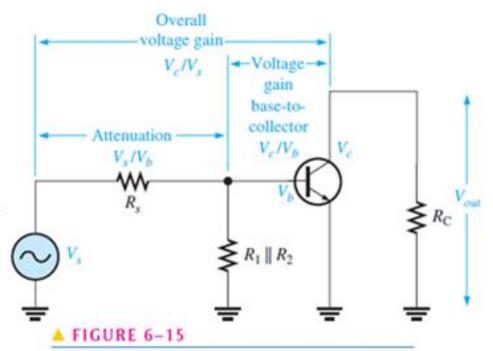
Attenuation

- Corresponds to a gain < 1
- Gain = 1/ attenuation
- If the signal amplitude is reduced by half, the attenuation is 2(gain of 0.5)

Attenuation =
$$\frac{V_s}{V_b} = \frac{R_s + R_{in(tot)}}{R_{in(tot)}}$$

The overall voltage gain of the amplifier, A'_v , is the voltage gain from base to collector, V_c/V_b , times the reciprocal of the attenuation, V_b/V_s .

$$A_{v}' = \left(\frac{V_{c}}{V_{b}}\right) \left(\frac{V_{b}}{V_{s}}\right) = \frac{V_{c}}{V_{s}}$$



Base circuit attenuation and overall voltage gain.

Effect of Bypass Capacitor

With Bypass C

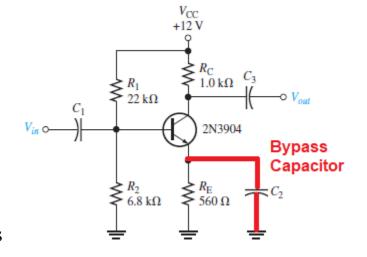
$$A_{v} = \frac{R_{\rm C}}{r'_{e}}$$

- Gain is **not stable** (r'e α Temp.)
- Voltage gain is max.

Without Bypass C

$$A_{\nu} = \frac{R_{\rm C}}{r_e' + R_{\rm E}}$$

- Gain is **stable**
- Voltage gain is less



EXAMPLE 6-5

Calculate the base-to-collector voltage gain of the amplifier in Figure 6–16 both without and with an emitter bypass capacitor if there is no load resistor. $r'_e = 6.58 \Omega$

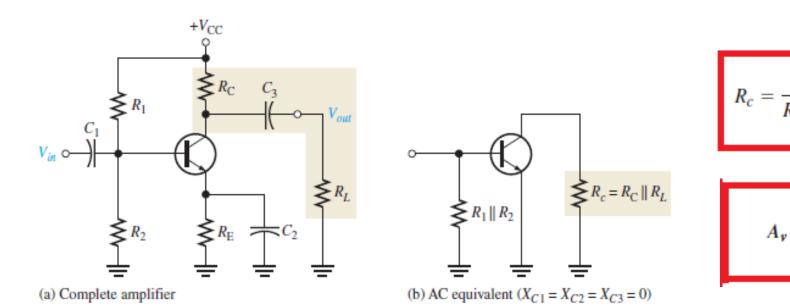
Without C2

$$A_v = \frac{R_{\rm C}}{r_e' + R_{\rm E}} = \frac{1.0 \,\mathrm{k}\Omega}{567 \,\Omega} = 1.76$$

With C2

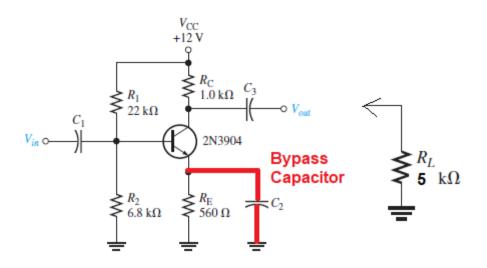
$$A_v = \frac{R_{\rm C}}{r_e'} = \frac{1.0 \,\mathrm{k}\Omega}{6.58 \,\Omega} = 152$$

Effect of Load on the Voltage Gain (Av)



- When Rc < RC, the voltage gain is reduced
- If RL >> RC → Rc ~ RC → Load has little effect on gain

Effect of Load on the Voltage Gain (Av)



EXAMPLE 6-6

Calculate the base-to-collector voltage gain of the amplifier in Figure 6–16 when a load resistance of $5 \text{ k}\Omega$ is connected to the output. The emitter is effectively bypassed and $r'_e = 6.58 \Omega$.

The ac collector resistance is

$$R_c = \frac{R_C R_L}{R_C + R_L} = \frac{(1.0 \,\mathrm{k}\Omega)(5 \,\mathrm{k}\Omega)}{6 \,\mathrm{k}\Omega} = 833 \,\Omega$$

$$A_v = \frac{R_c}{r_e'} = \frac{833 \ \Omega}{6.58 \ \Omega} = 127$$
 Rc < RC \rightarrow gain reduced

Note: The unloaded gain was found to be 152 in Example 6–5.