

## **Academic Quality Assurance Department**

## **Course Syllabus Form**

College	Information Technology				
Department	Computer Science				
Program	Computer Science, Data science, IS and MIT				
Course Title	Digital circuit design Course Number: 190412				
Year	2024	Semester:	2 <sup>nd</sup> semester		
Prerequisite(s)	Discrete mathematics				
Instructor	Dr. Saleh Salous				
Instructor's e-mail	s.salous@ptuk.edu.ps				
Office Hours	Sun, Tue, Thu [1-2] Mon, Wed [11-12]				
Class Time	Mon, Wed [12.30-2] Sun/Tue/Thu, [1-2]	Classroom:	E225		
	Sun/Tues/Thu, [2-3]				
Course description	This course will cover Number systems and conversion, Boolean algebra, the assertion level concept; minterm and maxterm expansions, Karnaugh maps, combinatorial logic circuit design, NAND and NOR gate based design. State machines and sequential circuits flip-flops, minimization of state tables, state assignment. Higher-level digital system design using SSI-MSI blocks such multiplexers/decoders, adders, memory and programmable gate arrays; bus oriented systems.				
Course Intended Learning Outcomes (CILOs)	At the end of this course, students should be able to 1.describe the physical operation and circuital model of a sensor 2. design simple circuits and implements them on breadboards 3. make records that accurately and clearly present the results obtained from laboratory work and allow them to be easily replicated by a third party.  4. express the algorithm in a programming language that can be executed on a microcontroller.				
Textbook(s)	M. Morris Mano. <u>Digital Design</u> . 5th Edition, Prentice Hall				
Other required material (References):					
Other Resources used (e.g. e-learning, field visits, periodicals, software, etc. )					

تاريخ الإصدار: 2019/5/12	رقم الإصدار: (1/0)	رمز الوثيقة: د.ج.أ- إ.ب.خ-ن02
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Course Teaching Methods			
Teaching Method	CILOs		
Online Lectures	1,2,3,4		

Assessment Type	Details/Explanation of assessment in relation to CILOs	Weight	Date(s)
Mid Exam	1,2	35%	
Activities	1,2,3,4	20%	As per college exam
Final Exam	1,2,3,4	45%	timetable
Total		100%	

Course Intended Learning Outcomes (CILOs)						
<u>CILOs</u>	Mapping to Program ILOs					
On successful completion of the course, students will be able to:	а	b		С	d	
describe the physical operation and circuital model of a sensor	х					
design simple circuits and implements them on breadboards		×		X		
make records that accurately and clearly present the results obtained from laboratory work and allow them to be easily replicated by a third party.		х		x	x	
express the algorithm in a programming language that can be executed on a microcontroller.		х			x	
Course Weekly Breakdown	1		T	1		
No. of Week		<b>Topics Covered</b>	CIL Os	Ass	essment	
2	Chap	ter 1:	1,4			
2.5	Chapter 2:		1,2	Mi	Mid Exam	
3.5	Chapter 3:		4,2			
Mid Exam						
2.5	Chap	ter 4:	2,3			
2.5	Chap	ter 5:	1,4	Fina	al Exam	
2	Chap	ter 6 :	2,4			

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Prepared by:	Dr. Saleh Salous	Signature	
<b>Head of Department</b>	Dr. Sherin Hijazi	Signature	
Date	29/02/2024		