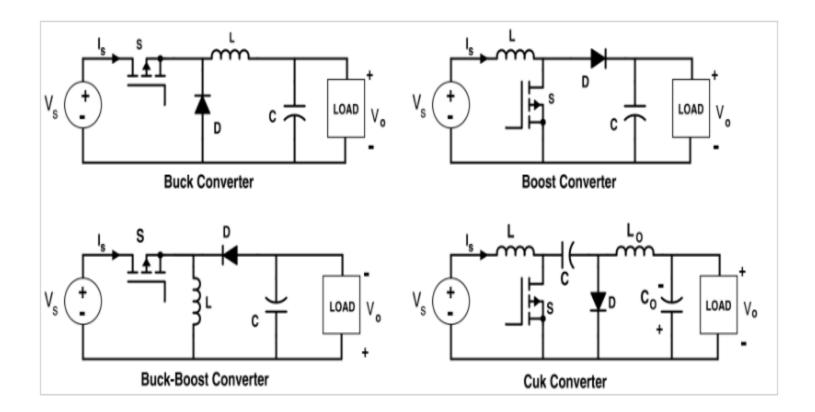
Power electronics lab

DC - DC Converter Buck /Boost /Buck Boost Converter

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The Pulse Width Modulation

The type of control most frequently used for DC generators and DC voltage converters is the pulse width modulation (PWM). Here the arithmetic average of a square wave voltage is influenced by the on-time ton being changed within a fixed period T. Another name for this method is pulse duration modulation

If, for example, you control the voltage on a load with this method, the arithmetic average of the load current IL will change according to the duty factor D. The following applies: D = ton/T

With a simple operational amplifier circuit, a drive circuit for generating a pulse modulation can be set up an operational amplifier compares the delta AC voltage UD with the control DC voltage Uctr. The operational amplifier is circuited as a comparator.

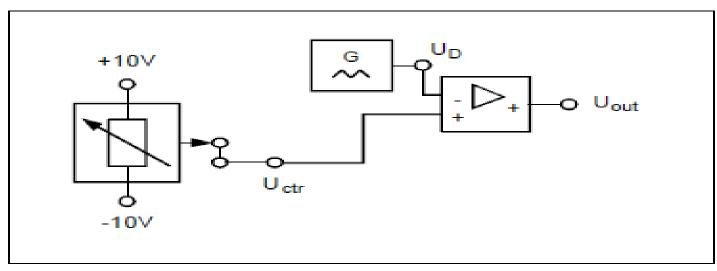


Fig. 5.1.1.2 Drive for PWM

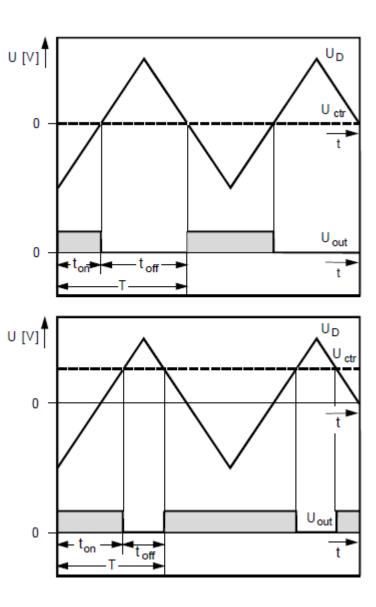
If the value of the control voltage U_{ctr} is greater than the value of the delta voltage U_D , the output of the OP switches to "high". If the control voltage U_{ctr} drops below the delta voltage U_D , the OP switches to "low". U_{out} is therefore a squarewave pulse sequence, whereby the pulse always appears in the time slot in which the delta voltage U_D is smaller than the control voltage U_{ctr} .

At $U_{ctr} = 0$ V this results in a pulse sequence with $t_{on} = \frac{1}{2} \cdot T$ or $t_{on} = t_{off}$.

The duty factor is $D = \frac{t_{on}}{T} = 0.5$.

If U_{ctr} is made more positive, the pulses get wider, the pauses narrower, the duty factor approaches the value 1 (DC voltage).

If U_{ctr} is made more negative, the pulses get narrower, the pauses wider and the duty factor smaller. The voltage U_{out} therefore serves as a basis for controlling a power final stage.

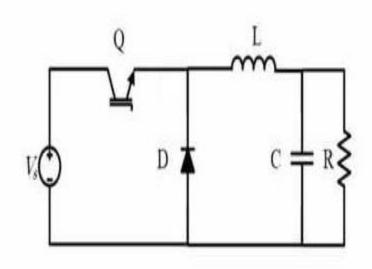


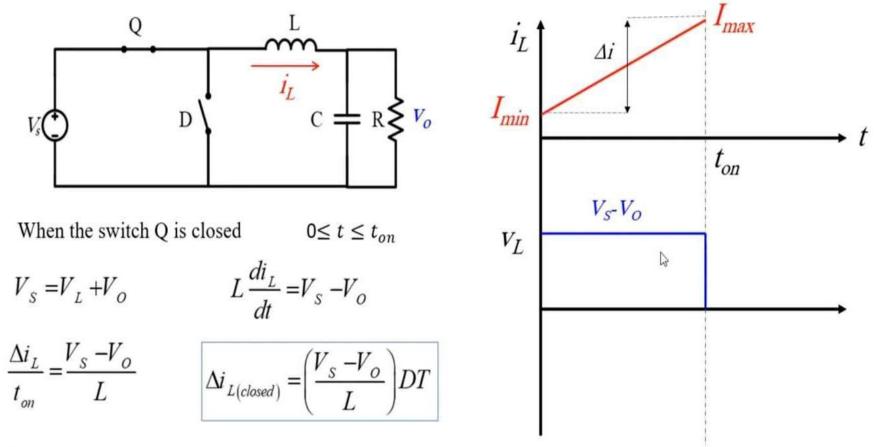
DC Regulators

- Convert a DC voltage, normally unregulated, to a regulated DC output voltage
- Achieved by pulse width modulation at fixed frequency
- Used power BJT, power MOSFET or IGBT
- In all converter types
 - LC filter: used to produce DC output with less ripple content
 - Freewheeling Diode: used to provide a path for inductor current when the main switch is open

DC Regulators...

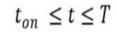
- The following assumptions are adopted before analysis of any type of DC regulators
 - 1) The circuit is operating in steady-state
 - 2) The inductor current is continuous
 - 3) The capacitor is very large and the output voltage is held constant
 - 4) The components are ideal

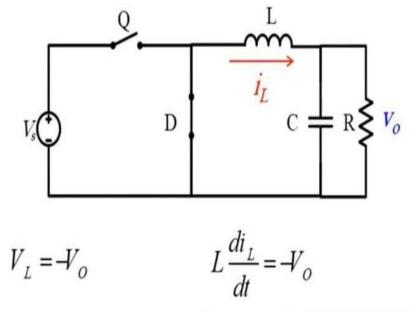




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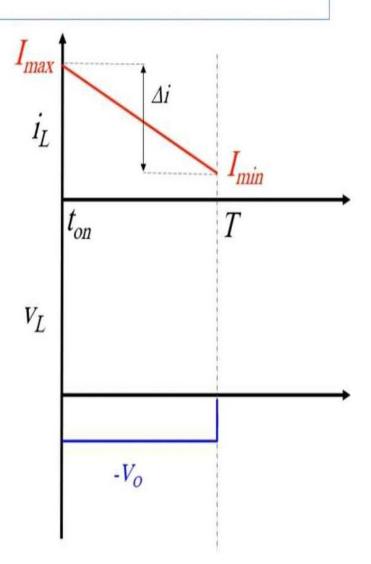
When the switch Q is opened

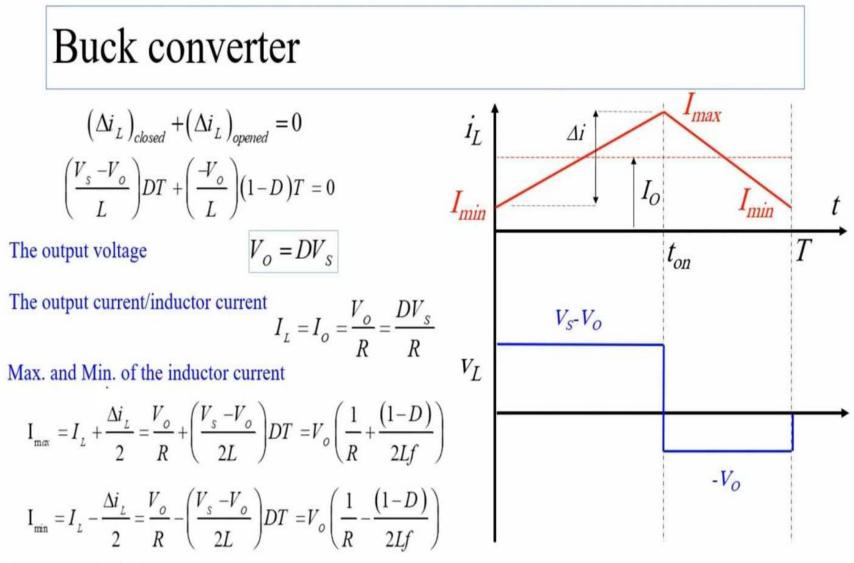




$$\frac{\Delta i_L}{T - t_{on}} = \frac{-V_O}{L}$$

$$\Delta i_{L(opened)} = \left(\frac{-V_o}{L}\right)(1-D)T$$





 $A \cap O \cap O \cap O \cap O$

The rms current of the inductor current

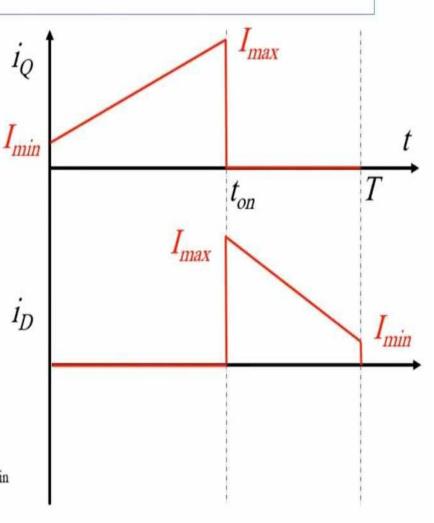
$$I_{L,rms} = \sqrt{I_L^2 + \left(\frac{\Delta i_L}{2\sqrt{3}}\right)^2}$$

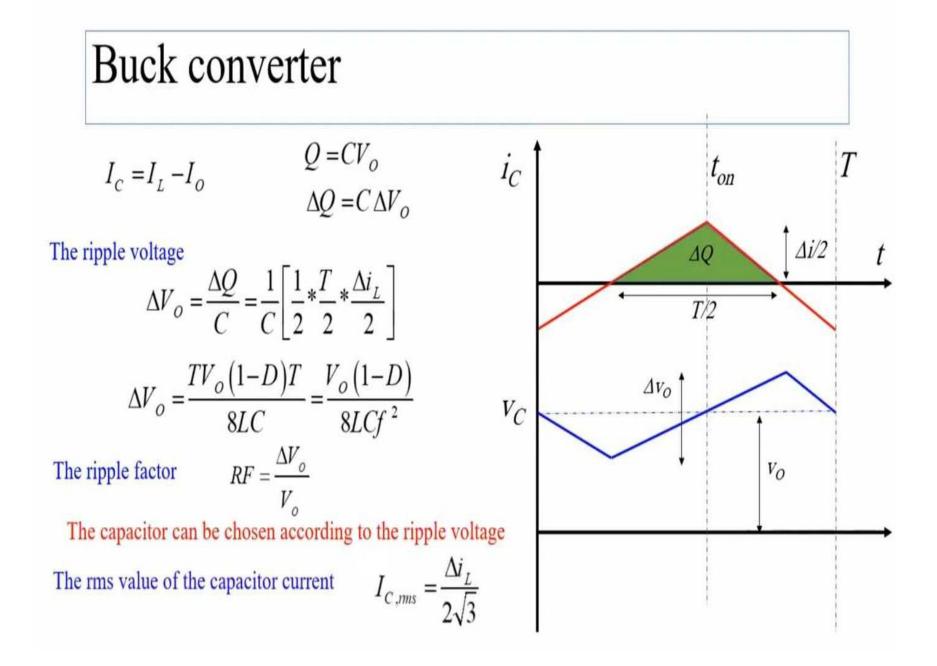
The switches currents

$$I_{Q,avg} = DI_L = I_S \qquad I_{Davg} = (1-D)I_L$$
$$I_{Q,ms} = \sqrt{D}I_{L,ms} = I_{S,ms} \qquad I_{D,ms} = \sqrt{(1-D)}I_{L,ms}$$

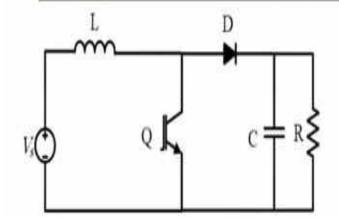
The minimum value of the inductance required for continuous current Operation $I_{min} = 0$

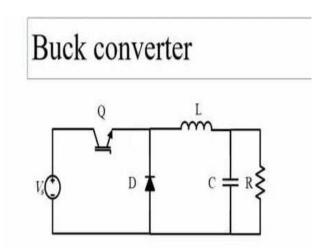
$$L_{\min} = \frac{(1-D)R}{2f} \qquad L_{des} = 1.25 * L_{\min}$$

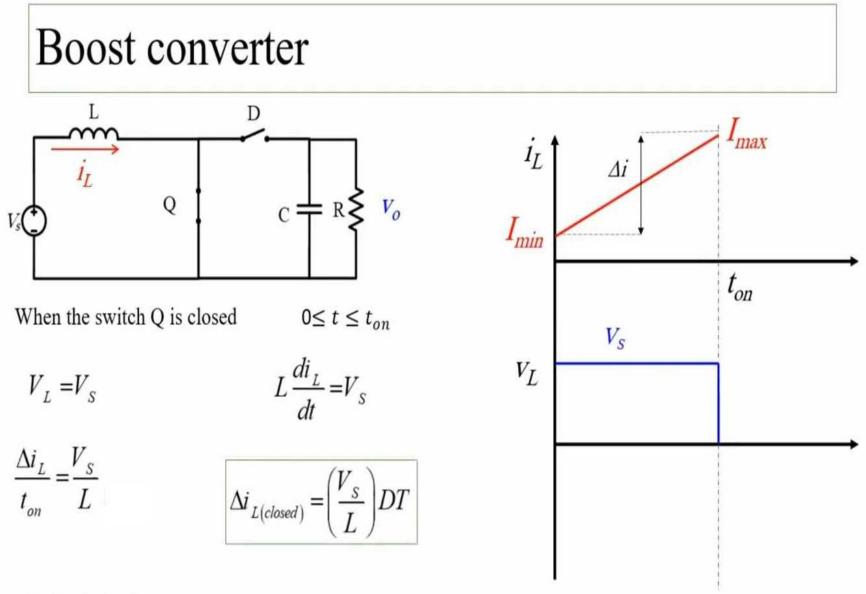






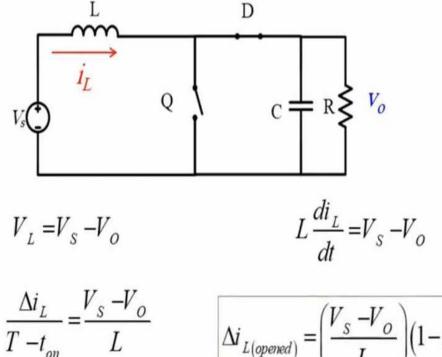


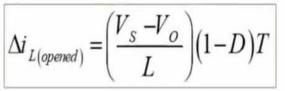


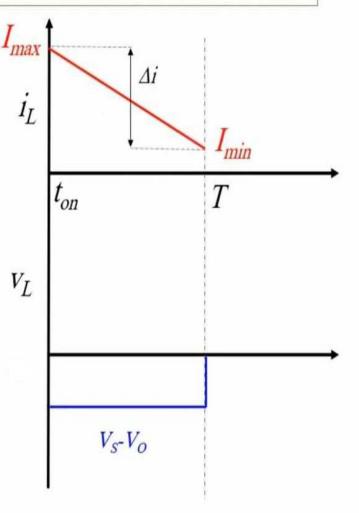


When the switch Q is opened

$$t_{on} \leq t \leq T$$







$$\left(\Delta i_L\right)_{closed} + \left(\Delta i_L\right)_{opened} = 0$$

$$\left(\frac{V_s}{L}\right)DT + \left(\frac{V_s - V_o}{L}\right)(1 - D)T = 0$$

The output voltage

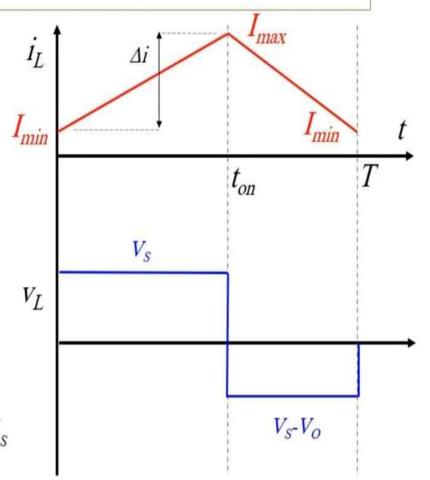
 $V_o = \frac{V_s}{1 - D}$

 $I_o = \frac{V_o}{R}$

The output current

The inductor current/ Supply current

$$I_{L} = \frac{I_{O}}{(1-D)} = \frac{V_{O}}{R(1-D)} = \frac{V_{S}}{R(1-D)^{2}} = I_{S}$$



Max. and Min. of the inductor current

$$I_{max} = I_L + \frac{\Delta i_L}{2} = \frac{V_S}{R(1-D)^2} + \frac{V_S D}{2Lf}$$
$$I_{min} = I_L - \frac{\Delta i_L}{2} = \frac{V_S}{R(1-D)^2} - \frac{V_S D}{2Lf}$$

The rms current of the inductor current

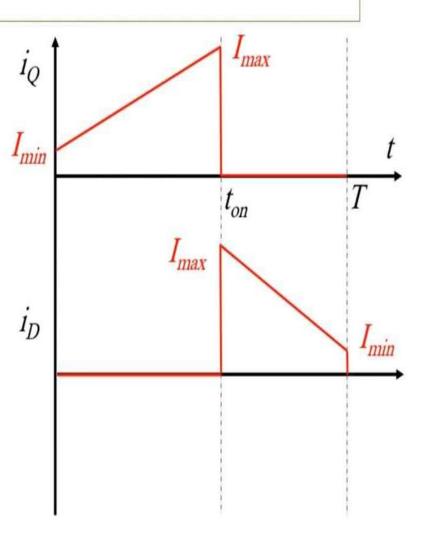
$$I_{L,ms} = \sqrt{I_L^2 + \left(\frac{\Delta i_L}{2\sqrt{3}}\right)^2}$$

The switches currents

 $I_{Q,avg} = DI_L \qquad I_{Davg} = (1-D)I_L$ $I_{Q,ms} = \sqrt{D}I_{L,ms} \qquad I_{D,ms} = \sqrt{(1-D)}I_{L,ms}$

The minimum value of the inductance required for continuous current Operation

$$I_{min} = 0$$
$$L_{min} = \frac{D(1-D)^2 R}{2f}$$
$$L_{des} = 1.25 * L_{min}$$



$$Q = CV_o$$
$$\Delta Q = C\Delta V_o$$

The ripple voltage

$$\Delta V_o = \frac{\Delta Q}{C} = \frac{I_o DT}{C} = \frac{V_o D}{RCf}$$

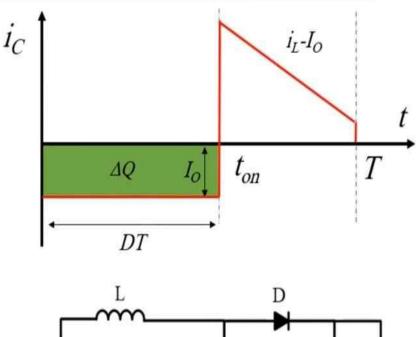
The ripple factor

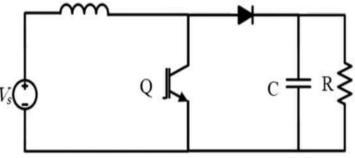
 $RF = \frac{\Delta V_o}{V_o}$

The capacitor can be chosen according to the ripple voltage

The rms value of the capacitor current

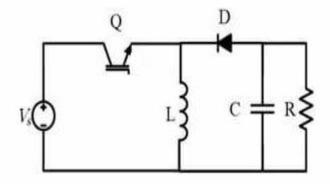
$$I_{C,ms} = \sqrt{I_{D,ms}^2 - I_{O,ms}^2} \qquad I_{O,ms} = I_O$$

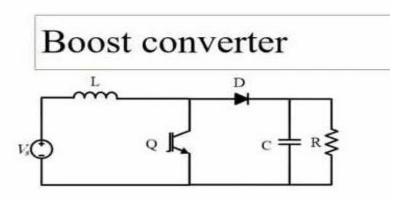


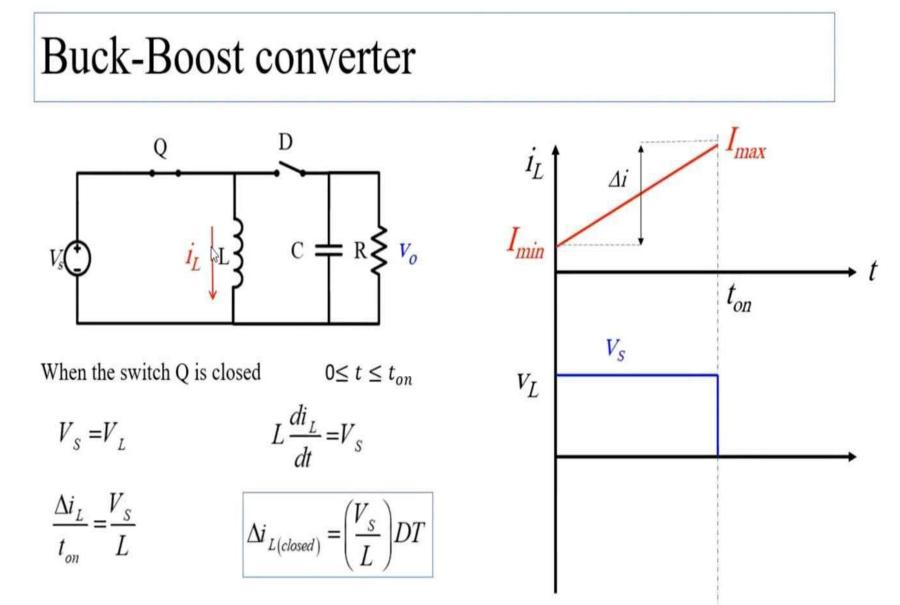


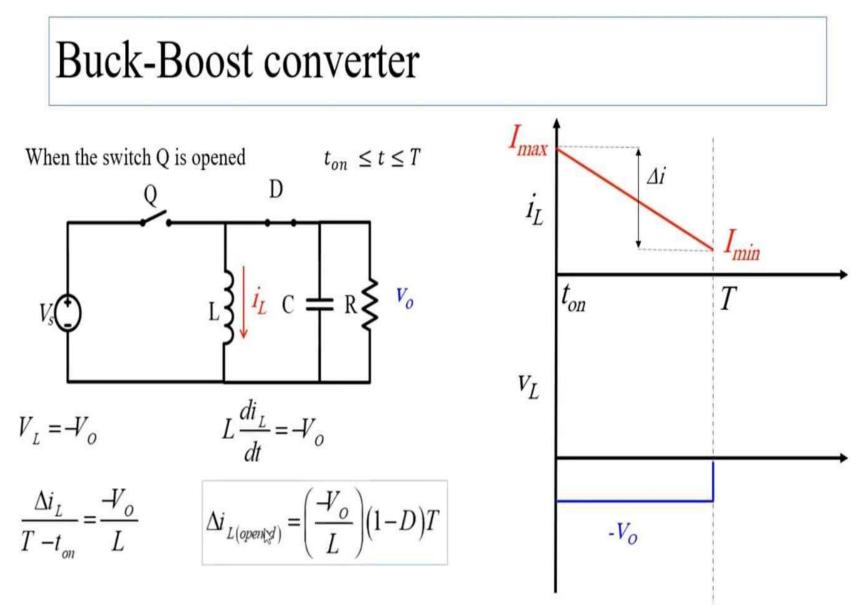
Buck-Boost converter

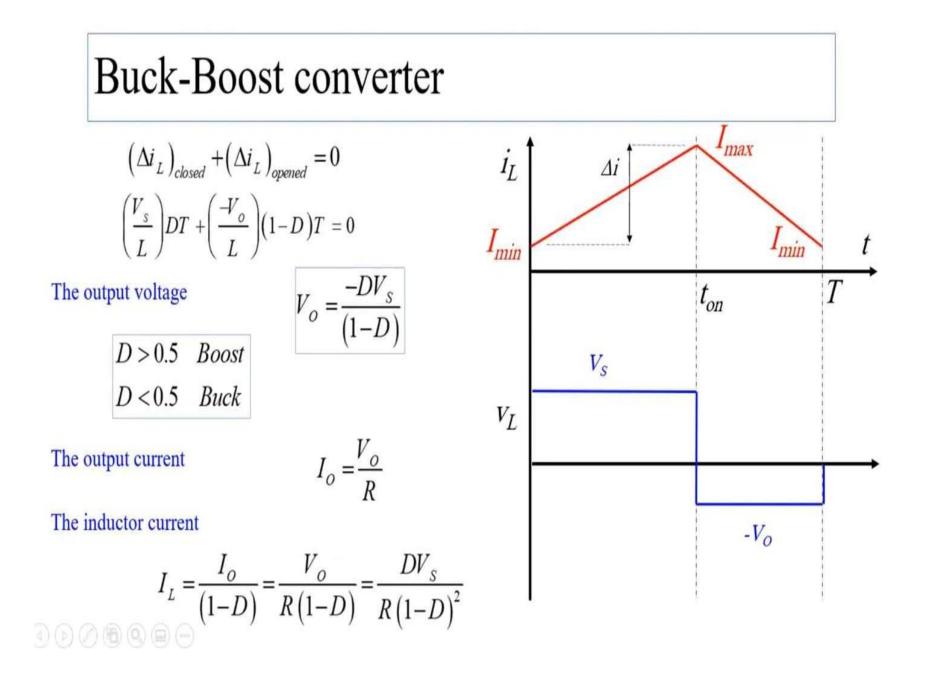
Buck-Boost converter











Buck-Boost converter

Max. and Min. of the inductor current

$$I_{max} = I_L + \frac{\Delta i_L}{2} = \frac{DV_S}{R(1-D)^2} + \frac{V_S D}{2Lf}$$
$$I_{min} = I_L - \frac{\Delta i_L}{2} = \frac{DV_S}{R(1-D)^2} - \frac{V_S D}{2Lf}$$

The rms current of the inductor current

$$I_{L,rms} = \sqrt{I_L^2 + \left(\frac{\Delta i_L}{2\sqrt{3}}\right)^2}$$

Buck-Boost converter

The switches currents

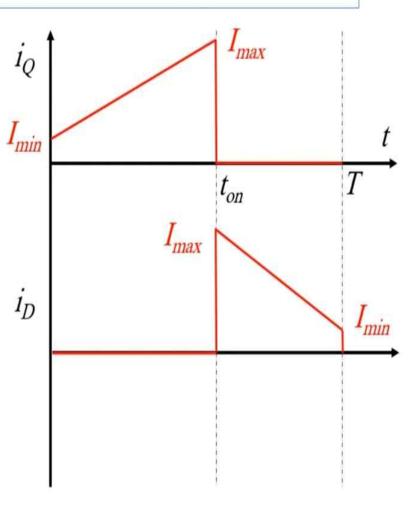
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The minimum value of the inductance required for continuous current Operation

$$I_{min} = 0$$

$$L_{min} = \frac{(1-D)^2 R}{2f}$$

$$L_{des} = 1.25 * L_{min}$$



Buck-Boost converter $Q = CV_o$ i_C $i_L - I_O$ $\Delta Q = C \Delta V_o$ The ripple voltage T $\Delta V_o = \frac{\Delta Q}{C} = \frac{I_o DT}{C} = \frac{V_o D}{RCf}$ ton ΔQ I_0 DT $RF = \frac{\Delta V_o}{V_o} = \frac{D}{RCf}$ The ripple factor D Q

R₹

С:

The capacitor can be chosen according to the ripple voltage