

UNTITLED - Proteus 8 Professional - Home Page

File System Help

PROTEUS DESIGN SUITE 8.0

Getting Started

- Schematic Capture
- PCB Layout
- Simulation
- Migration Guide

Help

- Help Home
- Schematic Capture
- PCB Layout
- Simulation

About

© 2014 Alexander Electronics, 1989-2017
 RELEASE 8.0 SP1 (BUILD 11417) with Advanced Navigation
 Manufacturer: In-

Free Memory: 214,096
 Windows X (x64) v6.11.7, 10/05/14/01

Start

Open Project **New Project** Import Legacy Design Open Sample

Recent Projects

- C:\Users\Walid Issa\Dropbox\Walid ESI\inverters_options\V_ses\sensors\Vsensord.pdspj
- C:\Users\Walid Issa\Dropbox\Walid ESI\inverters_options\V_ses\sensors\sensors.pdspj
- C:\ProgramData\Labcenter Electronics\Proteus 8 Professional\SAMPLES\Generator Scripts\QPSK Modulation.pdspj

News

New Version Available

Description	Release Date	USC Valid	
Proteus Professional 8.6 SP3 [8.6.23669]	24/03/2017	Yes	Download
Proteus Professional 8.5 SP1 [8.5.22252]	21/07/2016	Yes	Download
Proteus Professional 8.4 SP0 [8.4.21079]	04/12/2015	Yes	Download

2 next → → → then

File Edit View Tool Design Graph Debug Library Template System Help

Schematic Capture x

PROBES

VOLTAGE
CURRENT
TAPE

IN V=5

R1 4.7k

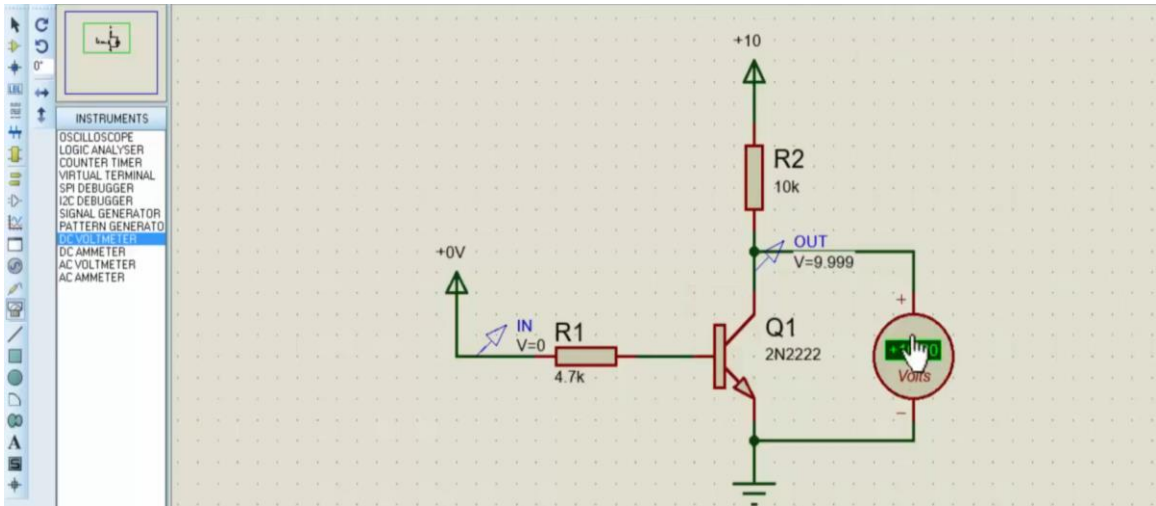
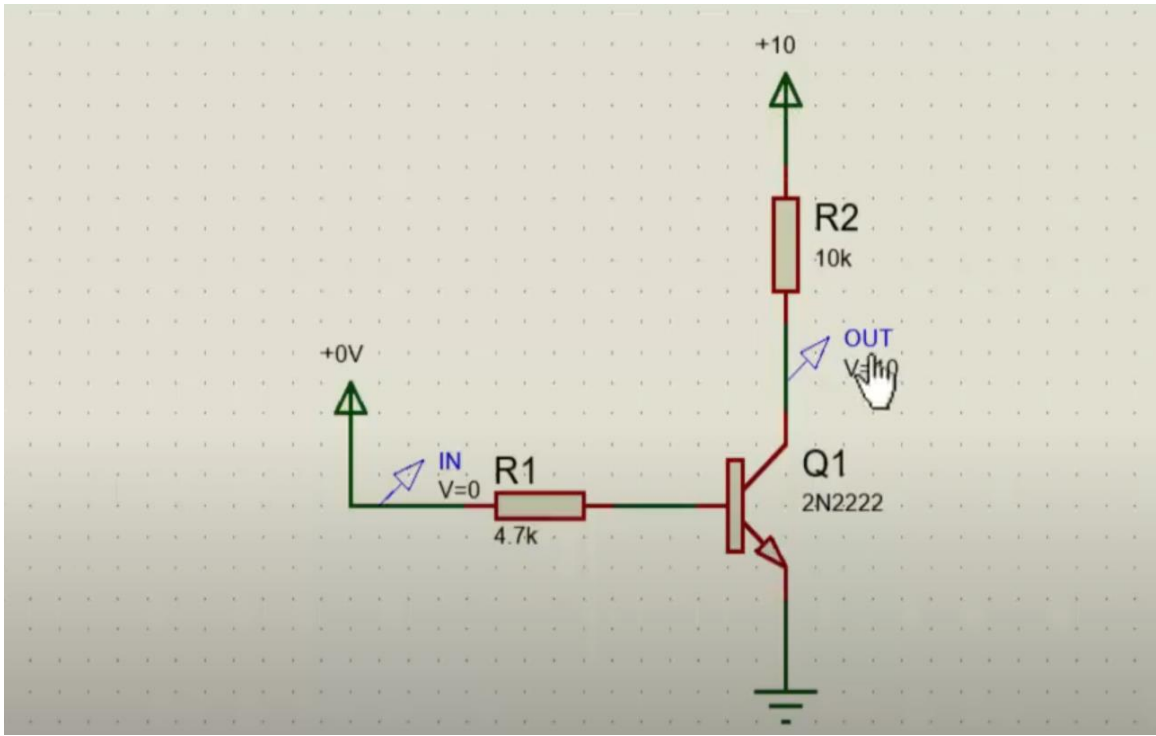
R2 10k

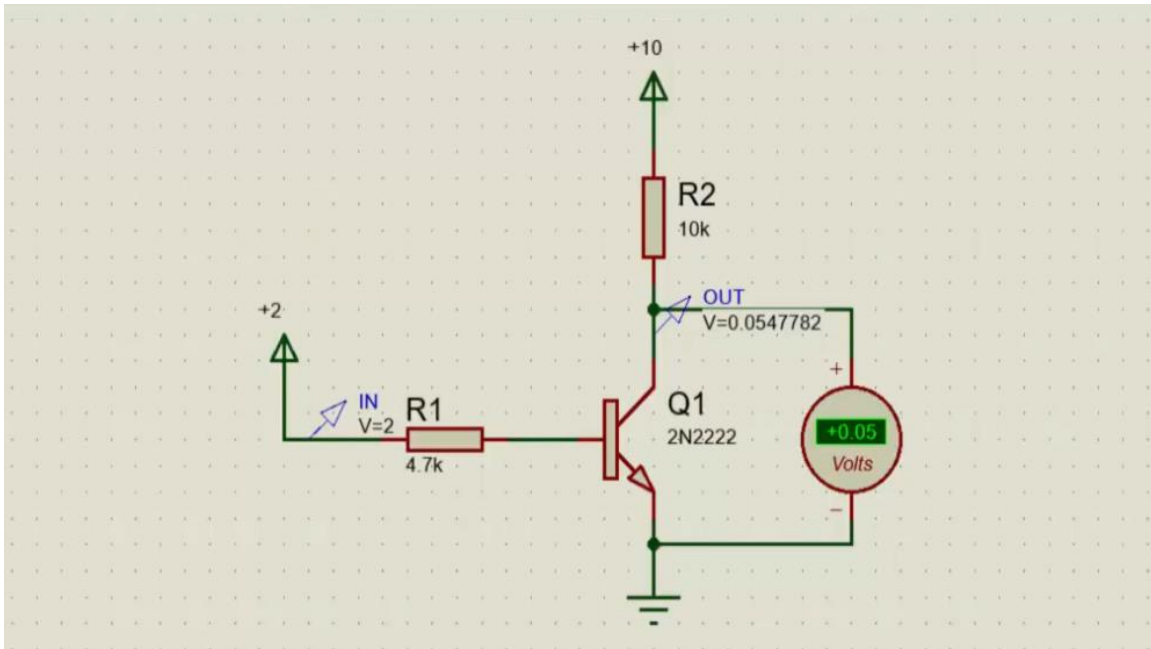
Q1 2N2222

OUT V=0.0443429

+10

2 Message(s) ANIMATING: 00:00:04.850000 (CPU load 1%) +300.0 +1300.0





test - Proteus 8 Professional - Schematic Capture

File Edit View Tool Design Graph Debug Library Template System Help

Schematic Capture x

GRAPHICS

COMPONENT

- PORT
- MARKER
- ACTUATOR
- INDICATOR
- VPROBE
- IPROBE
- TAPE
- GENERATOR
- TERMINAL
- SUBCIRCUIT
- 2D GRAPHIC
- WIRE DOT
- WIRE
- BUS WIRE
- BORDER
- TEMPLATE

Edit 2D Graphics Text

String: _____

Justification: Horizontal: Left Centre Right; Vertical: Top Middle Bottom

Graphic's Style: COMPONENT

Line Width: _____ Follow Global?

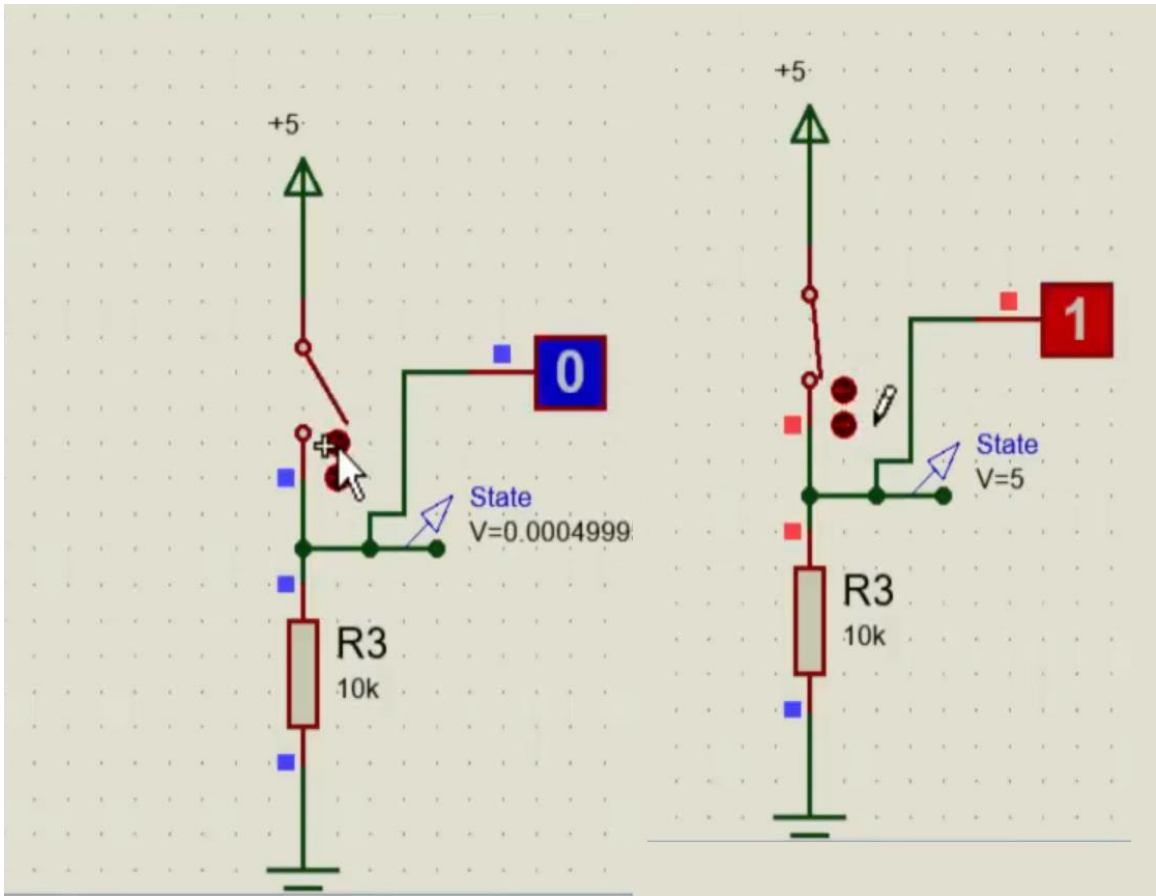
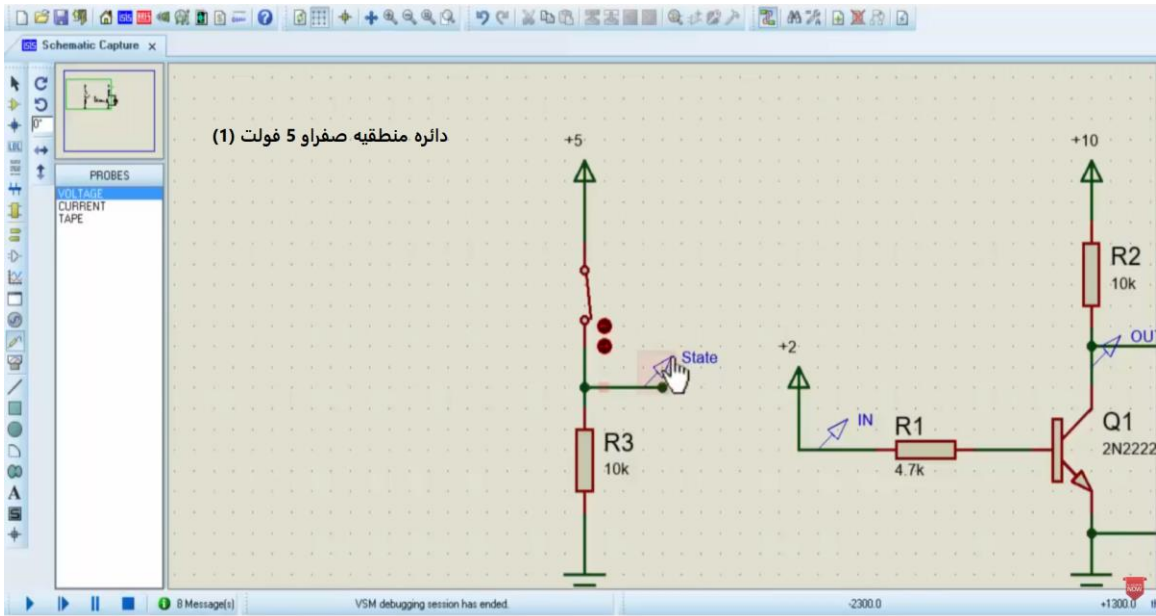
Colour: _____ Follow Global?

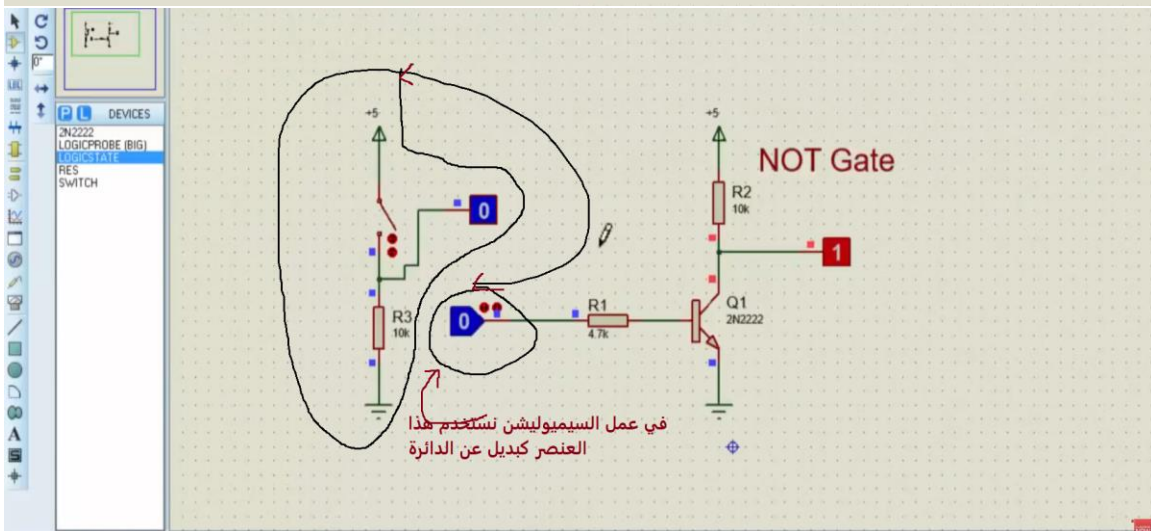
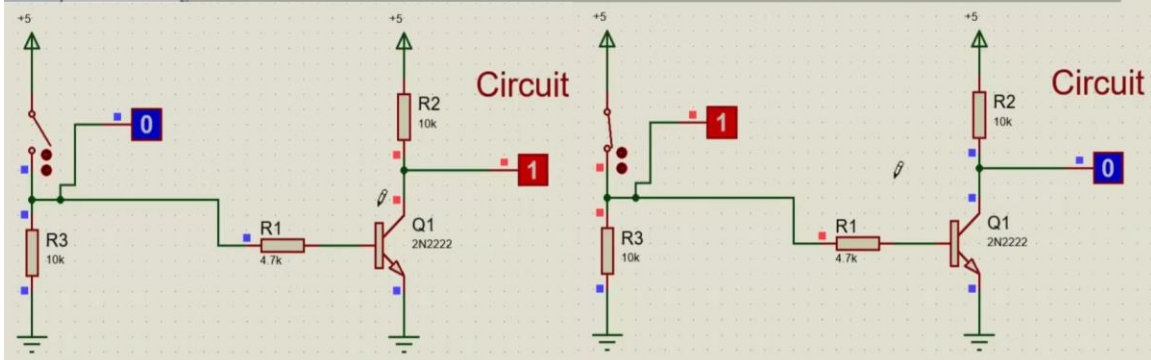
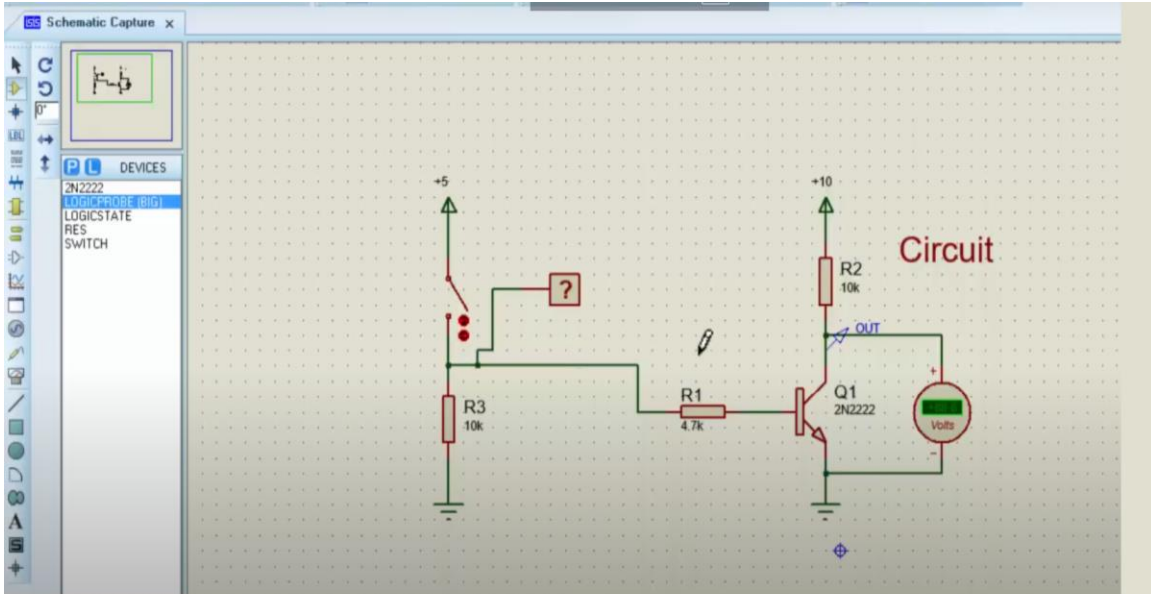
Font Attributes: Font face: Default Font; Height: 0.1in; Bold? ; Italic? ; Underline? ; Strikeout?

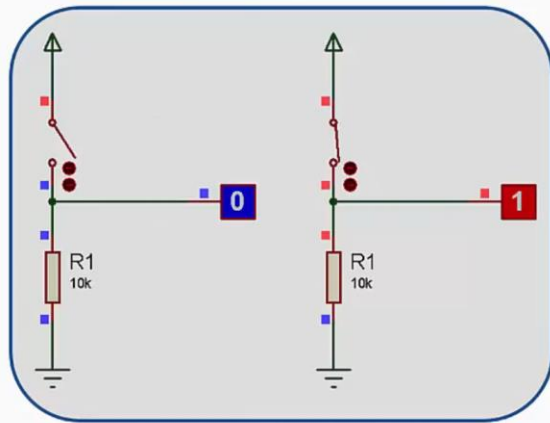
Sample: ABC abc XYZ xyz 123

OK Cancel

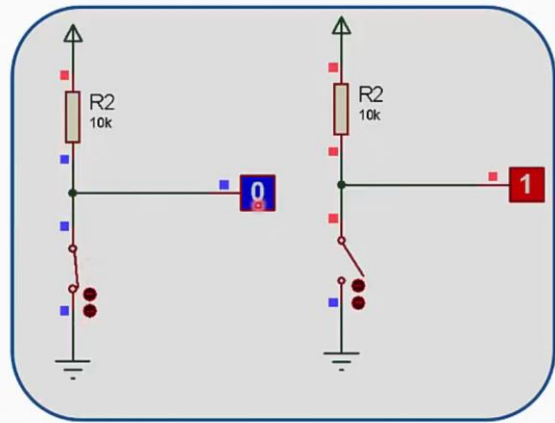
Circuit







Active High



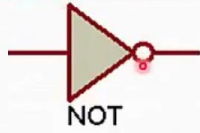
Active Low

البوابة المنطقية :

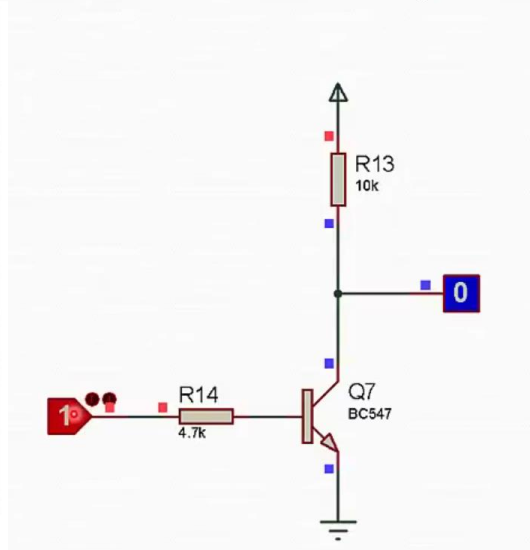
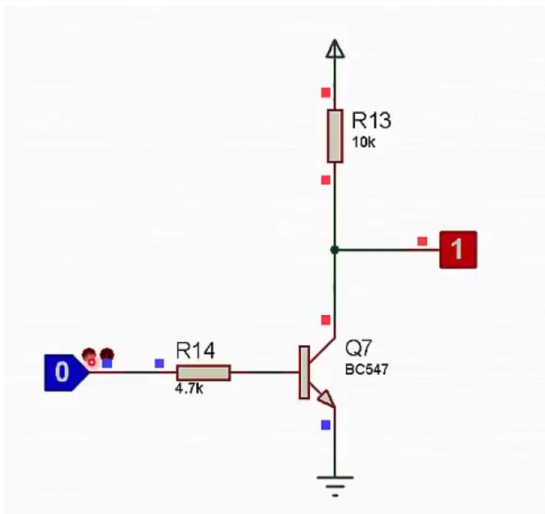
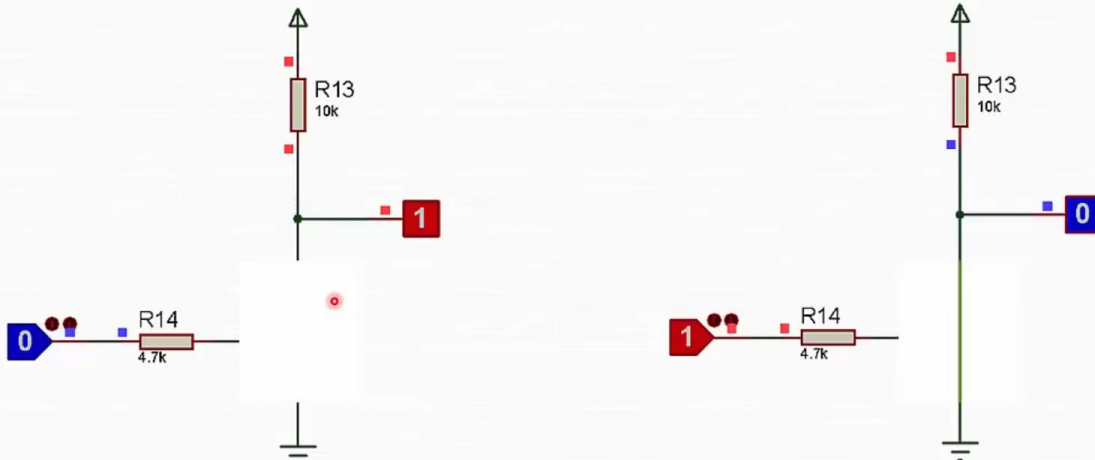
هي عبارة عن دائرة الكترونية لها مداخل ومخرج واحد فقط وتتعامل فقط بالحالات المنطقية 1 أو 0

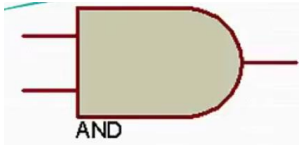


NOT Gate بوابة NOT



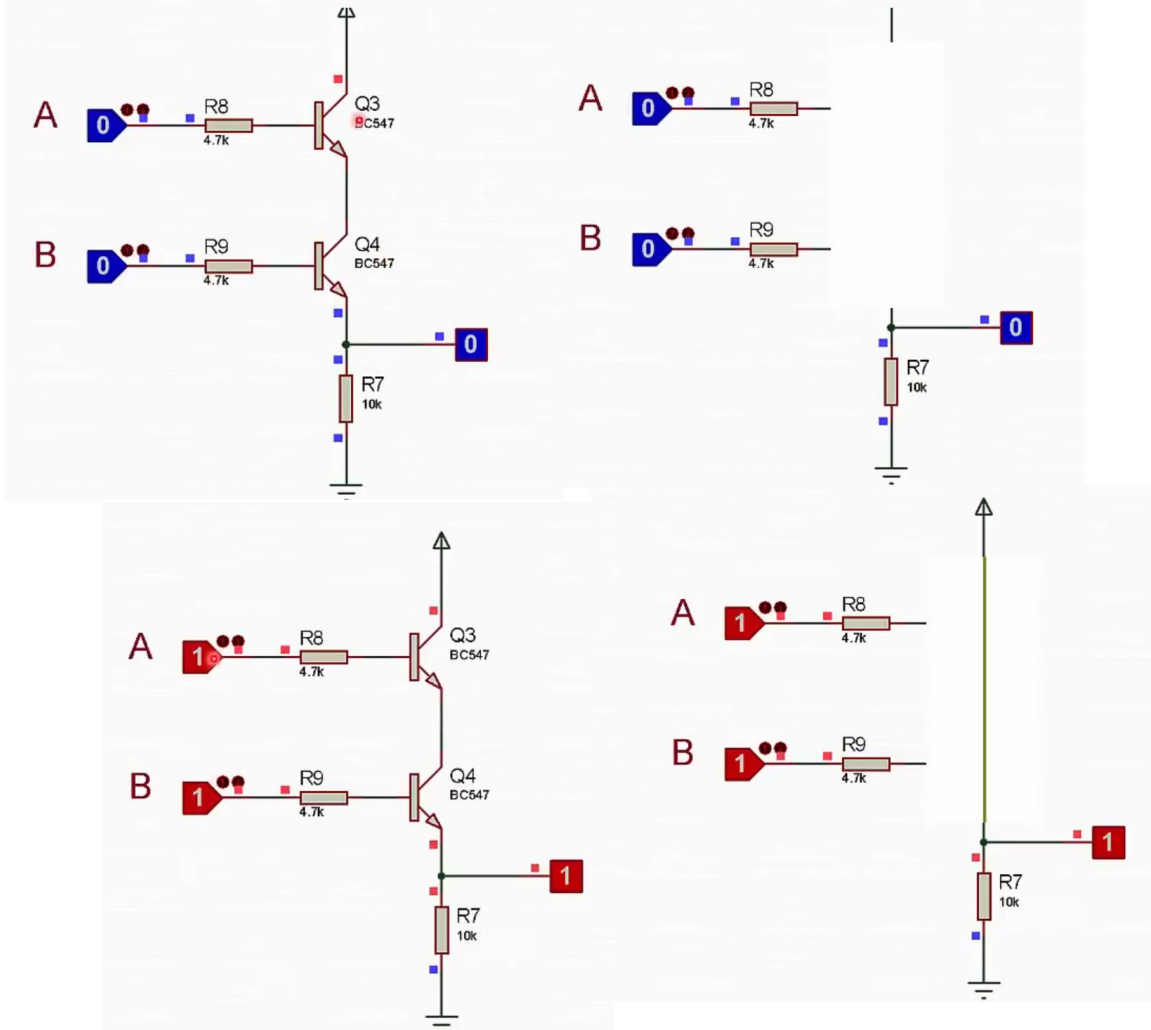
تقوم بعكس الدخل

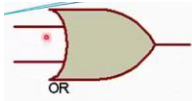




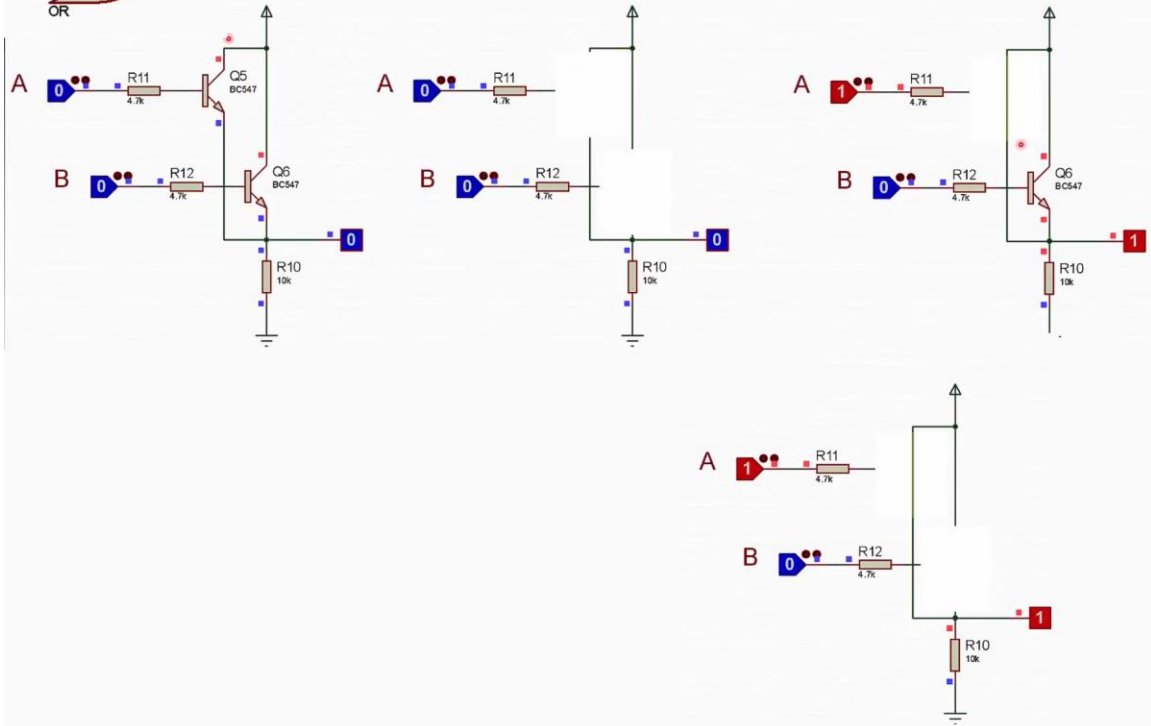
AND Gate بوابة AND

A AND B



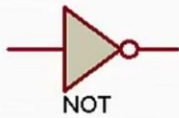


OR Gate بوابة OR

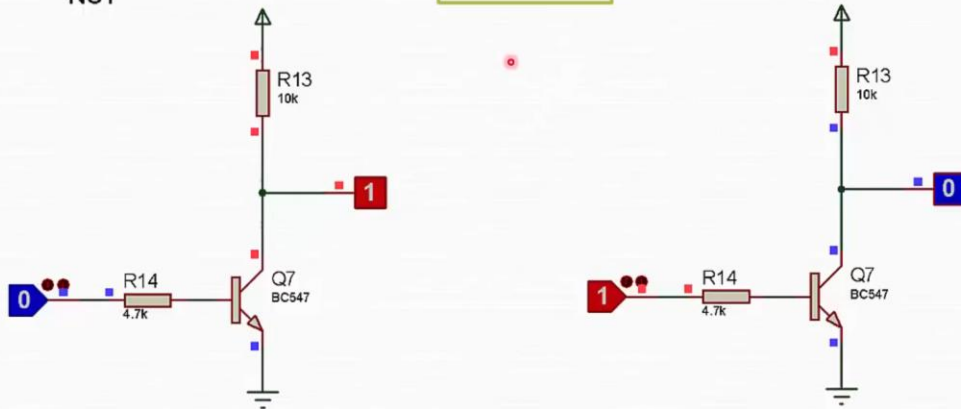


NOT Gate

ما نوع الترانزستورات المستخدمة ؟

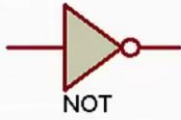


BJT

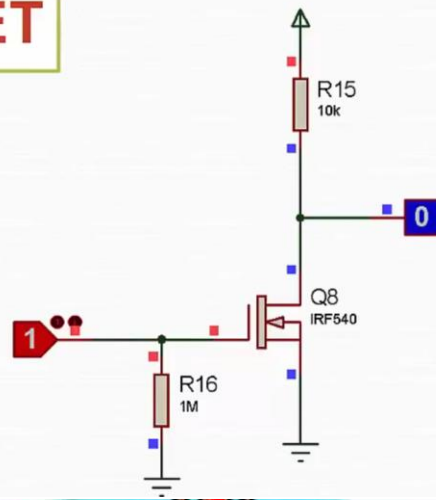
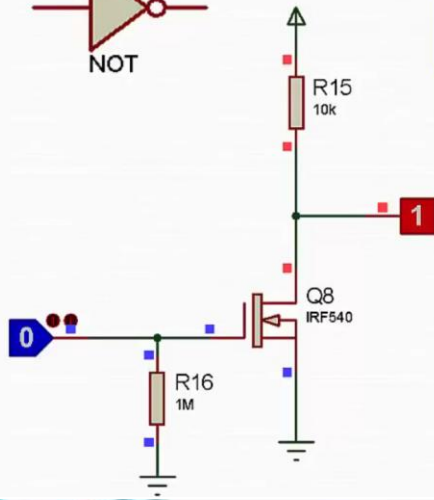


NOT Gate

ما نوع الترانزستورات المستخدمة ؟



MOSFET

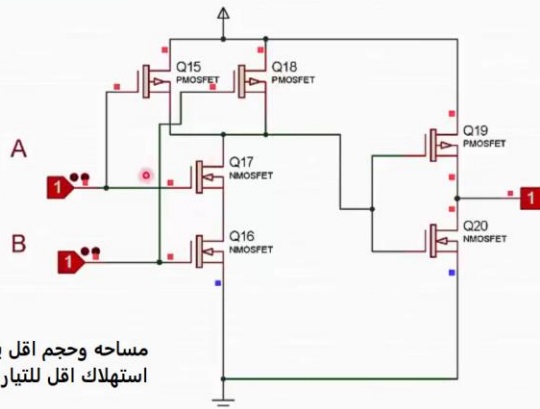
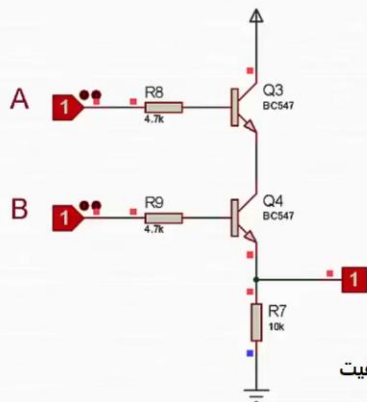


AND Gate

ما نوع الترانزستورات المستخدمة ؟

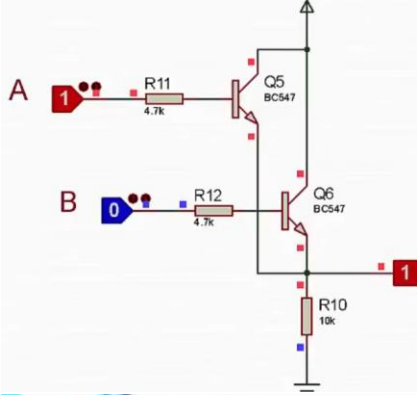
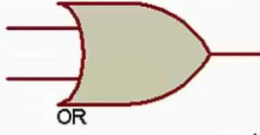


MOSFET



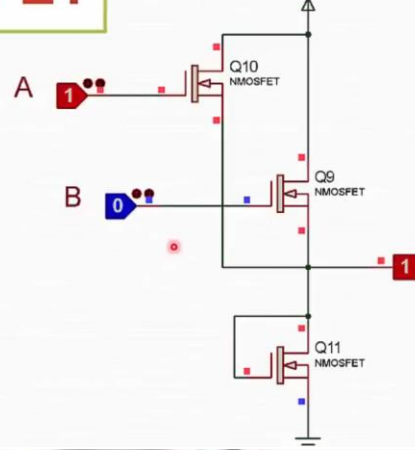
مساحه وحجم اقل بسبب قلة المقاومات
استهلاك اقل للتيار تقريبا صفر في الموسفيت

OR Gate



ما نوع الترانزستورات المستخدمة ؟

MOSFET



TTL

Transistor-Transistor Logic

BJT



لا تتعامل إلا مع 5 فولت

أسرع

تستهلك طاقة اكبر

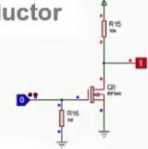
غير حساسة

74XXX

CMOS

Complementary Metal Oxide Semiconductor

MOSFET



يمكنها التعامل مع جهود اعلى ، مثلا 15 فولت

صعوبة في التعامل مع ترددات كبيرة - أبطأ

تستهلك طاقة صغيرة جدا

حساسة للكهرباء الاستاتيكية

4XXX